

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 1207**
Katsuhiro NAKAI et al. : Attorney Docket No. 2004_1946A
Serial No. 10/517,258 : Group Art Unit 2136
Filed December 7, 2004 : Examiner C. C. Okoronkwo
SEMICONDUCTOR INTEGRATED :
CIRCUIT DEVICE, DATA STORAGE :
VERIFICATION DEVICE, AND DATA :
STORAGE VERIFICATION METHOD :

RESPONSE TO RESTRICTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the requirement set forth in the Office Action mailed March 18, 2008, Applicants hereby elect invention I, claims 1-18.

In view of this election, a full examination on the merits of the present application is respectfully requested.

Respectfully submitted,

Katsuhiro NAKAI et al.

/Jeffrey R. Filipek/

By: **2008.04.18 13:40:49 -04'00'**

Jeffrey R. Filipek
Registration No. 41,471
Attorney for Applicants

JRF/fs
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
April 18, 2008